

**Listing Of Claims**

1. (Currently Amended) A liquid crystal display device including a data line supplied with a data signal, a gate line supplied with a scanning signal, a pixel electrode for driving a liquid crystal cell, and a thin film transistor for responding to the scanning signal to switch the data signal to the pixel electrode, the device comprising:

a storage electrode having a first region overlapping the gate line to form a storage capacitor, and a second region offset from and parallel to the gate line;

a first protective layer having non-contiguous portions at respective ends of the storage electrode in a layer between the storage electrode and the pixel electrode, wherein the storage electrode is directly connected to the pixel electrode [[at]] on an entire surface of the second region of the storage electrode [[exclusive of]] in areas not including the first protective layer; and

a second protective layer formed between a gate insulating film and the pixel electrode.

2. (Previously Presented) The liquid crystal display device according to claim 1, wherein said storage capacitor includes:

the gate insulating film on a substrate in such a manner to cover the gate line; and

a first semiconductor layer between the gate insulating film and the storage electrode.

3. (Previously Presented) The liquid crystal display device according to claim 1, wherein the first protective layer is formed at side edges of the storage electrode.

4. (Previously Presented) The liquid crystal display device according to claim 2, further comprising:

a gate electrode contacting the gate line on the substrate;

a second semiconductor layer on the gate insulating film over the gate electrode; and

a source electrode and a drain electrode on the second semiconductor layer.

5. (Previously Presented) The liquid crystal display device according to claim 4, wherein the second semiconductor layer includes an active layer and an ohmic contact layer, and the active layer is patterned simultaneously with the second protective layer and the ohmic contact layer is patterned simultaneously with the source electrode and the drain electrode.

6. (Currently Amended) A liquid crystal display device including a data line supplied with a data signal, a gate line supplied with a scanning signal, a pixel electrode for driving a liquid crystal cell, and a thin film transistor for responding to the scanning signal to switch the data signal to the pixel electrode, the device comprising:

a storage electrode having a first region overlapping the gate line to form a storage capacitor, and a second region offset from and parallel to the gate line; and

the pixel electrode covering an upper surface and side edges of the storage electrode, wherein the storage electrode is directly connected to the pixel electrode [[at]] on an entire surface of the second region of the storage electrode.

7. (Previously Presented) The liquid crystal display device according to claim 6, wherein the storage capacitor includes:

the gate line formed on a substrate;  
a gate insulating film formed on the substrate to cover the gate line; and  
a first semiconductor layer formed on the gate insulating film and simultaneously patterned with the storage electrode.

8. (Previously Presented) The liquid crystal display device according to claim 6, further comprising:

a gate electrode connected with said gate line on said substrate;  
a gate insulating film on said substrate;  
a second semiconductor layer on said gate insulating film;  
a source electrode and a drain electrode on said second semiconductor layer;  
a protective layer on said gate insulating film; and  
the pixel electrode on said protective layer.

9. (Previously Presented) The liquid crystal display device according to claim 8, wherein the second semiconductor layer includes an active layer and an ohmic contact layer, the active layer being patterned simultaneously with the protective layer and the ohmic contact layer being patterned simultaneously with the source electrode and the drain electrode.

10. (Withdrawn) A method of fabricating a liquid crystal display device, comprising:
- forming a gate line on a substrate;
  - forming a gate insulating film on said substrate to cover the gate line;
  - forming an ohmic contact layer and a storage electrode by entirely depositing a first semiconductor layer, a second semiconductor layer and a metal layer onto said gate insulating film and then simultaneously patterning the second semiconductor layer and the metal layer into the same pattern;
  - forming an active layer and a protective layer by depositing an insulating material onto the first semiconductor layer and then patterning the first semiconductor layer and the insulating material; and
  - forming a pixel electrode by depositing a transparent conductive material onto the gate insulating film and then patterning the transparent conductive material.
11. (Withdrawn) The method according to claim 10, wherein the protective layer is separately formed at each lower edge of the storage capacitor area.
12. (Withdrawn) The method according to claim 10, further comprising:
- forming a gate electrode on the substrate;
  - forming a gate insulating film on the substrate;
  - forming an ohmic contact layer and source and drain electrodes on the gate insulating film in the same pattern simultaneously;
  - forming an active layer and a protective layer on the gate insulating film in the same pattern simultaneously; and
  - forming a pixel electrode on the protective layer.
13. (Withdrawn) A method of fabricating a liquid crystal display device, comprising:
- forming a gate line on a substrate;
  - forming a gate insulating film on the substrate in such a manner to cover the gate line;
  - forming an ohmic contact layer and a storage electrode by entirely depositing a first semiconductor layer, a second semiconductor layer and a metal layer onto the gate insulating film and then simultaneously patterning the second semiconductor layer and the metal layer into the same pattern;

forming an active layer by depositing an insulating material onto the first semiconductor layer and then simultaneously patterning the first semiconductor layer and the insulating material; and

forming a pixel electrode by depositing a transparent conductive material onto the gate insulating film at an area extended into an upper edge and each side edge of the storage electrode and then patterning the transparent conductive material.

14. (Withdrawn) The method according to claim 13, further comprising:

forming a gate electrode on the substrate;

forming a gate insulating film on the substrate to cover the gate electrode;

forming an ohmic contact layer and source and drain electrodes on the gate insulating film in the same pattern simultaneously;

forming an active layer and a protective layer on the gate insulating film in the same pattern simultaneously; and

forming a pixel electrode on the gate insulating film.

15. (Currently Amended) A liquid crystal display device, comprising:

first and second substrates;

a gate line and a data line over the first substrate, the data line crossing the gate line to define a pixel region;

a thin film transistor having source and drain electrodes at the crossing of the gate line and data line;

a storage electrode having a first region over the gate line, and a second region offset from and parallel to the gate line;

a pixel electrode over the storage electrode;

a first protective layer [[patterned on]] having non-contiguous portions at respective ends of the storage electrode in a layer between the storage electrode and the pixel electrode, wherein the pixel electrode directly connects to the storage electrode [[at]] on an entire surface of the second region [[exclusive of]] in areas not including the first protective layer;

a second protective layer formed between a gate insulating film and the pixel electrode; and

a liquid crystal layer between the first and second substrates.

16. (Currently Amended) The liquid crystal display device of claim 15, wherein ~~[[a]] the pixel electrode of an adjacent pixel region extends~~ surrounds an area over the storage electrode.

17. (Previously Presented) The liquid crystal display device of claim 15, wherein a storage capacitor is formed between the storage electrode and the gate line and wherein the first protective layer overlaps a portion of the storage capacitor.

18. (Previously Presented) The liquid crystal display device of claim 15, wherein a storage capacitor is formed between the storage electrode and the gate line and wherein the first protective layer overlaps a lower edge of the storage capacitor.

19. (Currently Amended) The liquid crystal display device of claim 16, wherein the pixel electrode ~~of the adjacent pixel region~~ is substantially rounded where the pixel electrode surrounds over the storage electrode.

20. (Previously Presented) The liquid crystal display device according to claim 1, wherein the first protective layer is simultaneously formed with the second protective layer.

21. (Previously Presented) The liquid crystal display device according to claim 2, wherein the semiconductor layer has an active layer and an ohmic contact layer, and the active layer is patterned simultaneously with the first protective layer and the ohmic contact layer is patterned simultaneously with the storage electrode.

22. (Previously Presented) The liquid crystal display device of claim 15, wherein the first protective layer is simultaneously formed with the second protective layer.